

AMENDMENTS TO THE DRAWINGS

A replacement sheet of drawings has been submitted that replaces FIGURES 5A and 5B with a single FIGURE 5. FIGURE 5 in the replacement sheet is the same as FIGURE 5A in the original drawings, except that "PARENT NODE" is replaced by --OWNER NODE.--



MEMORY EFFICIENT OCCURRENCE MODEL DESIGN FOR
VLSI CAD

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Application Serial No. 09/709,695

REPLACEMENT SHEET

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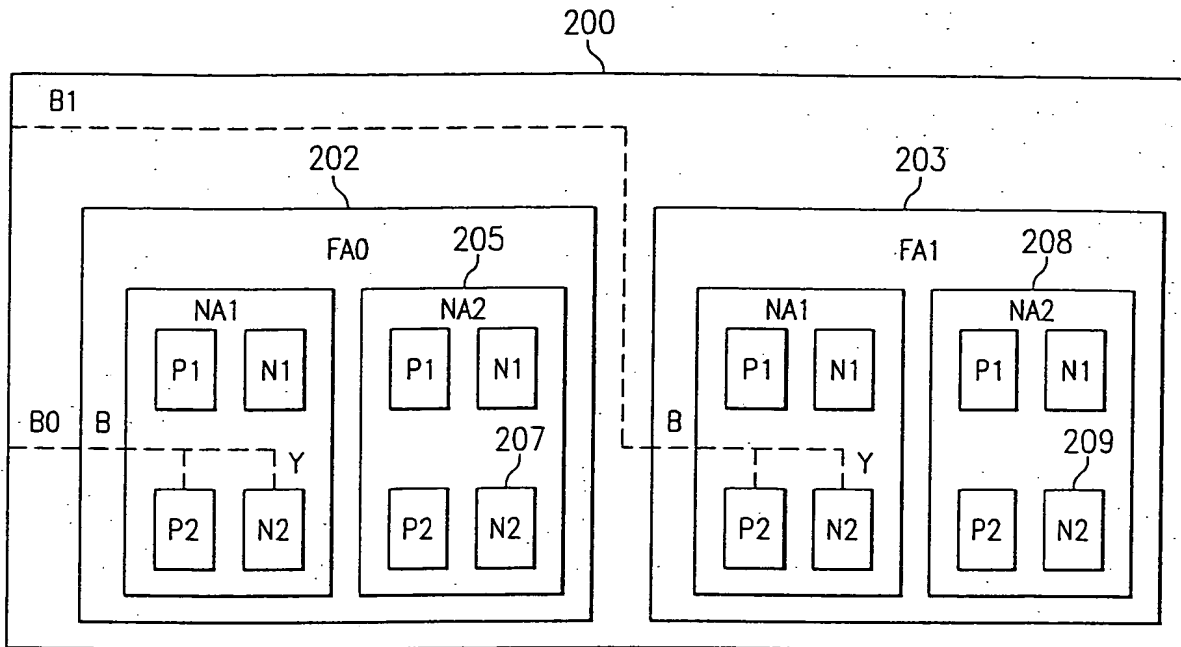


FIG. 3
(PRIOR ART)

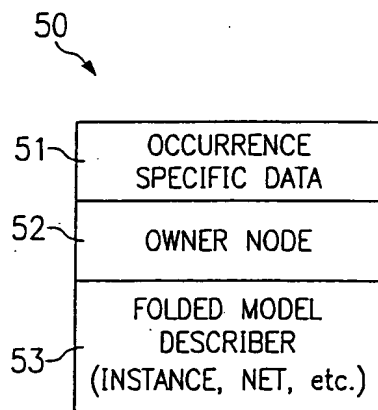


FIG. 5